What Is Claimed Is:

1. A liquid crystal display panel, driven by a column inversion driving mode, comprising:

a plurality of parallel data lines, disposed along an X-axis;

a plurality of parallel scan lines, disposed along a Y-axis;

a plurality of display unit pixels, each having a control transistor, a storage capacitor, a common electrode, and a pixel electrode, wherein each set of two adjacent data lines and two adjacent scan lines defines the boundary of one display unit pixel wherein each data line interlaces with both scan lines; and

a plurality of display unit blocks disposed in array, each having display unit pixels parallel to and disposed sequentially along two adjacent data lines, wherein the display unit pixels within any display unit block between two adjacent data lines are coupled to one of the two adjacent data lines, while the display unit pixels within another adjacent display unit block between the two adjacent data lines are coupled to the other of the two adjacent data lines.

2. The liquid crystal display panel as claimed in claim 1, wherein the display unit pixels within any display unit block in a row and the display unit pixels within another adjacent display unit block in the row are coupled to different data lines.

3. The liquid crystal display panel as claimed in claim 1, wherein the common electrode and the pixel electrode make up a liquid capacitor.

4. A liquid crystal display panel, driven by a column inversion driving mode, comprising:

a plurality of data lines having a first data line and a second data line;

a plurality of scan lines having a first scan line and a second scan line;

a plurality of display unit pixels, each having a control transistor, a storage capacitor, a common electrode, and a pixel electrode, wherein each set of two adjacent parallel data lines and two adjacent parallel scan lines defines the boundary of one display unit pixel wherein each data line interlaces with both scan lines;

a first display unit block disposed between the first and the second data lines and having the display unit pixels parallel to and disposed sequentially along the first and the second data lines, wherein the display unit pixels are coupled to the first data line; and

a second display unit block disposed between the first and the second data lines, adjacent to the first display unit block, and having the display unit pixels parallel to and disposed sequentially along the first and the second data lines, wherein the display unit pixels are coupled to the second data line.

5. The liquid crystal display panel as claimed in claim 4, further comprising:

a third data line;

a third scan line;

a third display unit block disposed between the second and the third data lines and having the display unit pixels parallel to and disposed sequentially along the second and the third data lines, wherein the display unit pixels are coupled to the second data line; and

a fourth display unit block disposed between the second and the third data lines, adjacent to the third display unit block, and having the display unit pixels parallel to and disposed sequentially along the second and the third data lines, wherein the display unit pixels are coupled to the third data line.

6. The liquid crystal display panel as claimed in claim 4, wherein the common electrode and the pixel electrode make up a liquid capacitor.

7. The liquid crystal display panel as claimed in claim 4, wherein the polarity of the first display unit block is opposite that of the second display unit block upon completion of the column inversion driving mode.

8. The liquid crystal display panel as claimed in claim 5, wherein the common electrode and the pixel electrode make up a liquid capacitor.

9. The liquid crystal display panel as claimed in claim 5, wherein upon completion of the column inversion driving mode, the polarity of the first display unit block is the same as that of the fourth display unit block, and polarities of both the second and the third display unit blocks are opposite those of the first display unit block.